

FIG. 1 is a block diagram of a computer system 10 in accordance with the present invention. The computer system 10 includes a CPU 16, a Memory Controller 18, Memory 12, an I/O Controller 14, a Network Interface 26, an Input Device 20, a CRT Display 22, and a Projector 24.

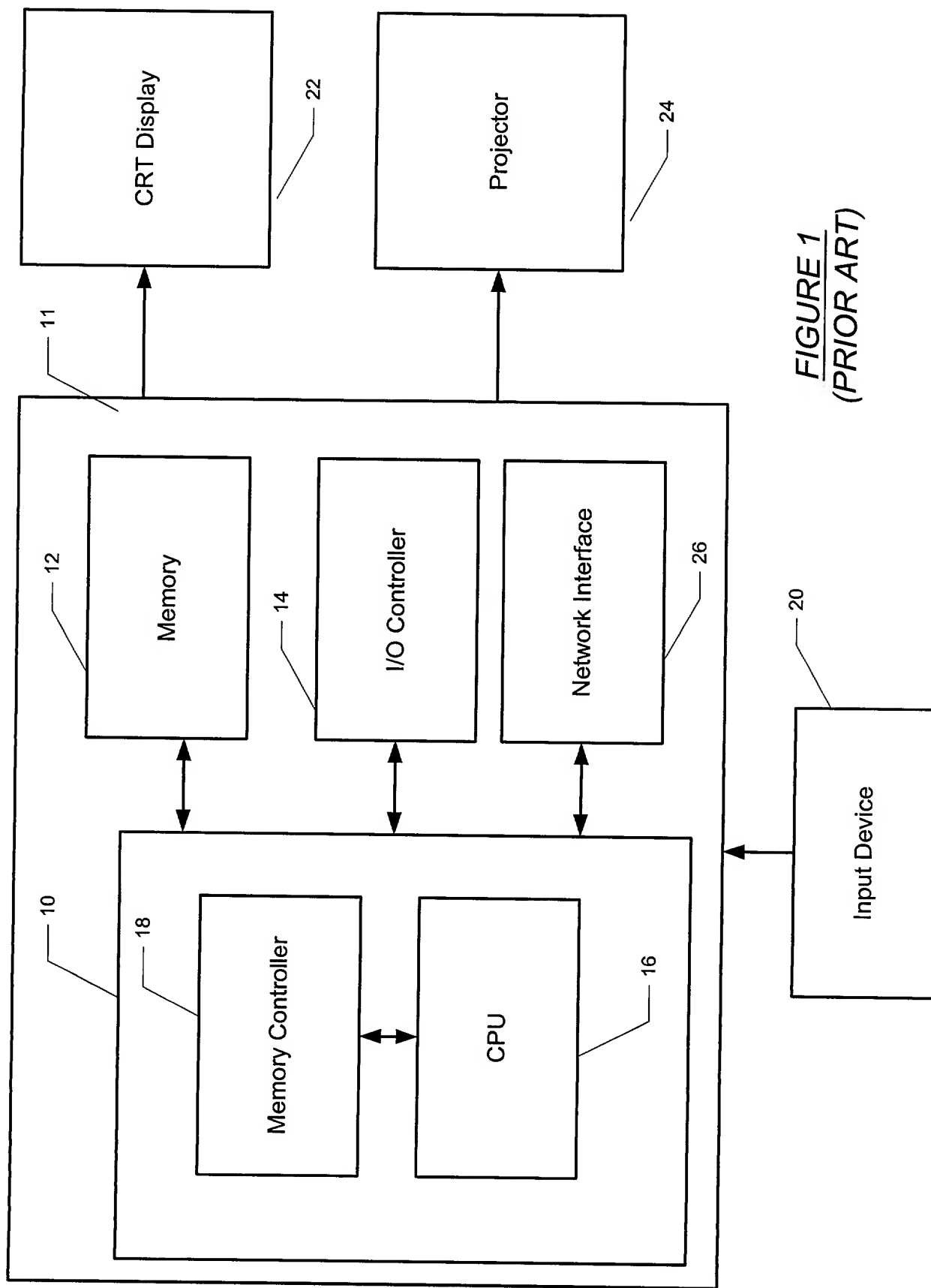


FIGURE 1
(PRIOR ART)

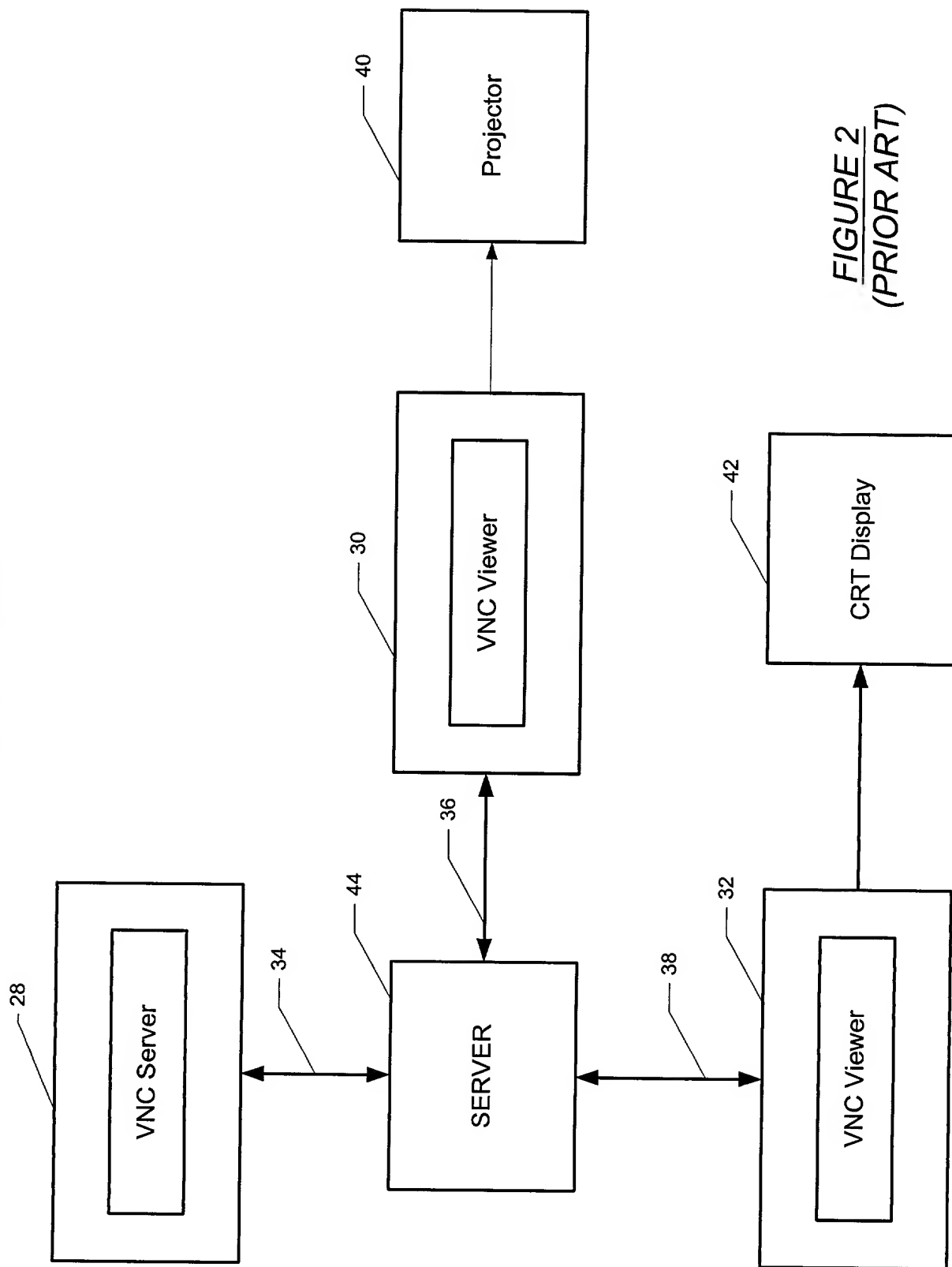


FIGURE 2
(PRIOR ART)

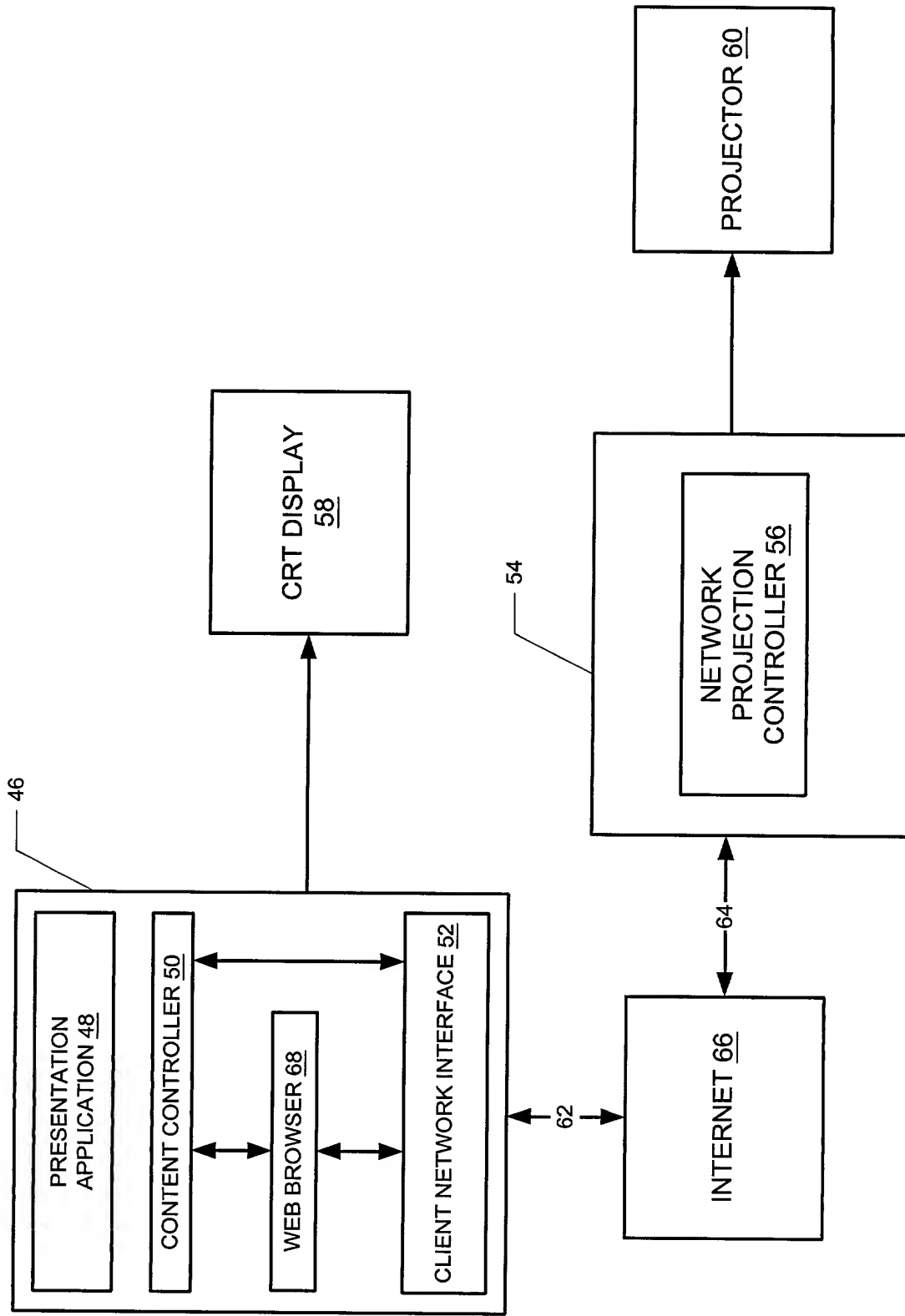


FIGURE 3

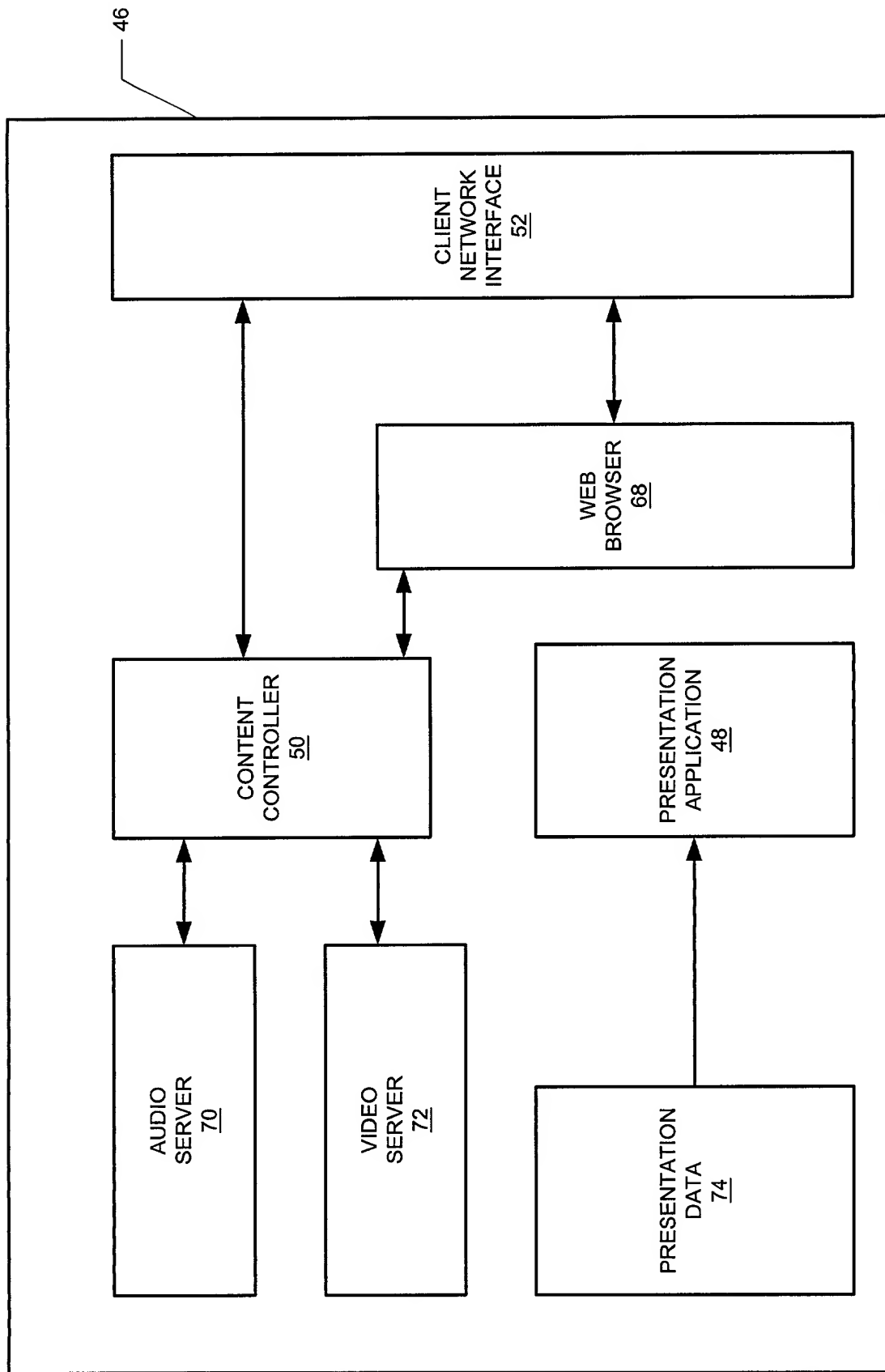


FIGURE 4

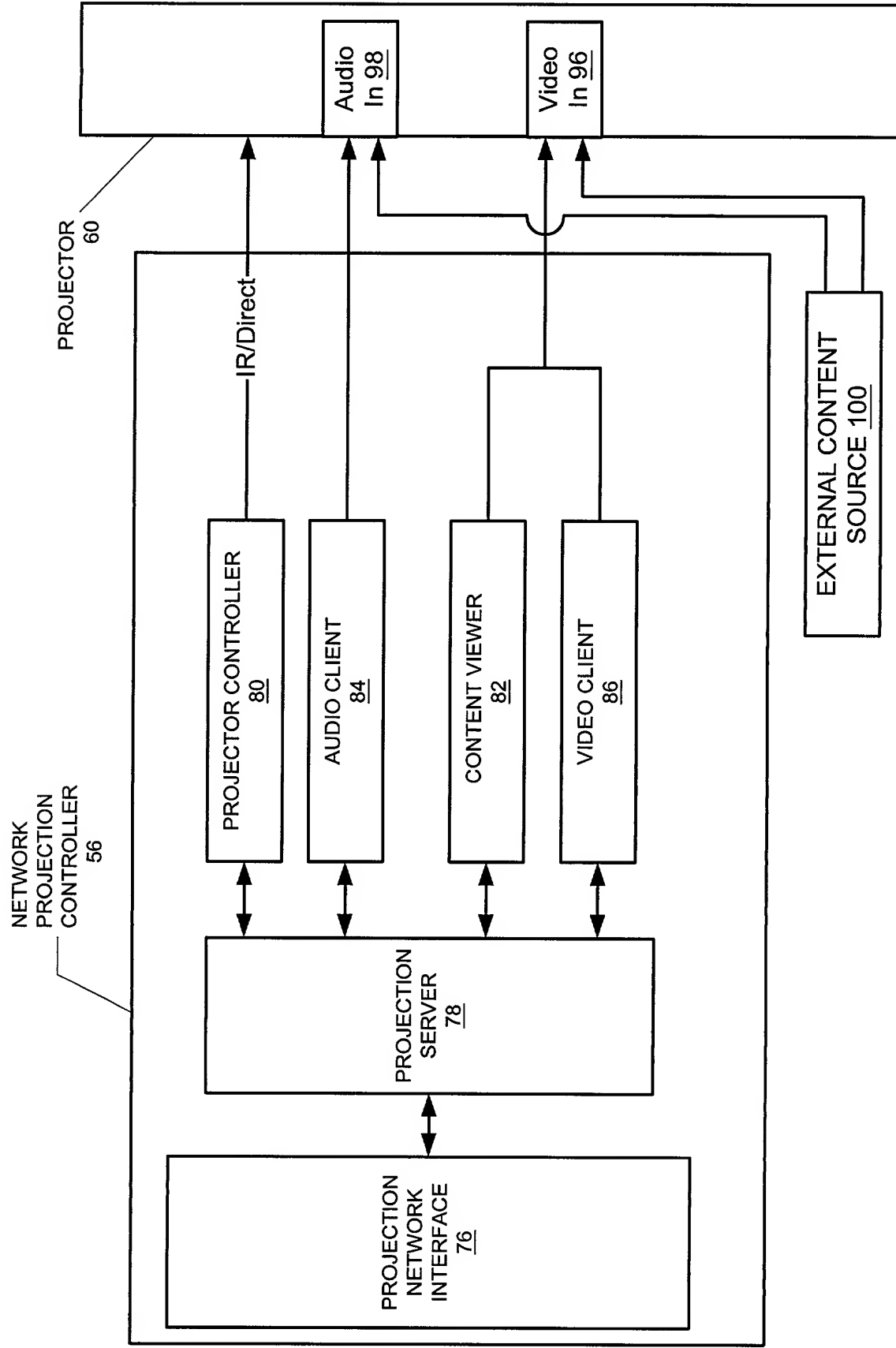


FIGURE 5

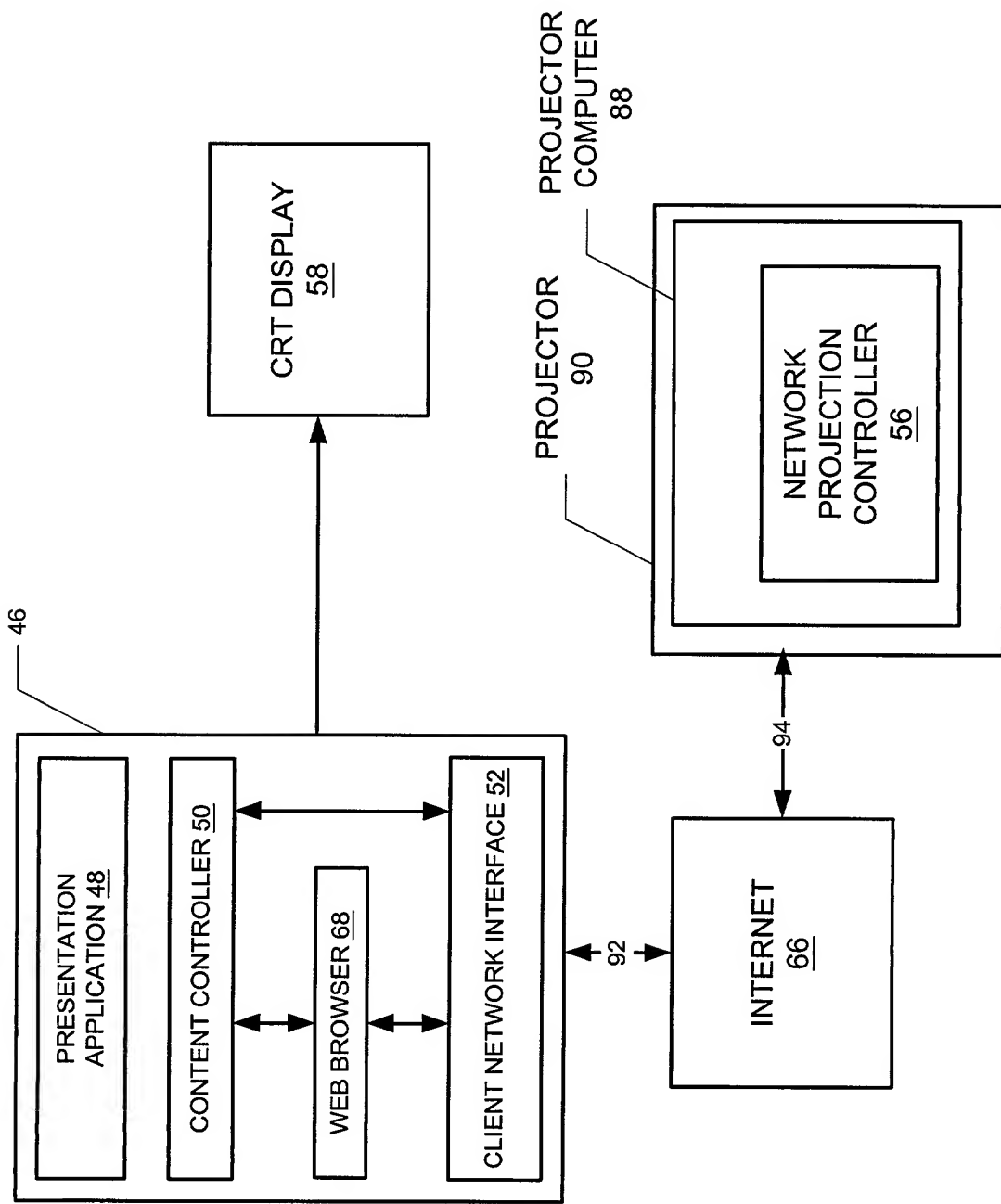


FIGURE 6

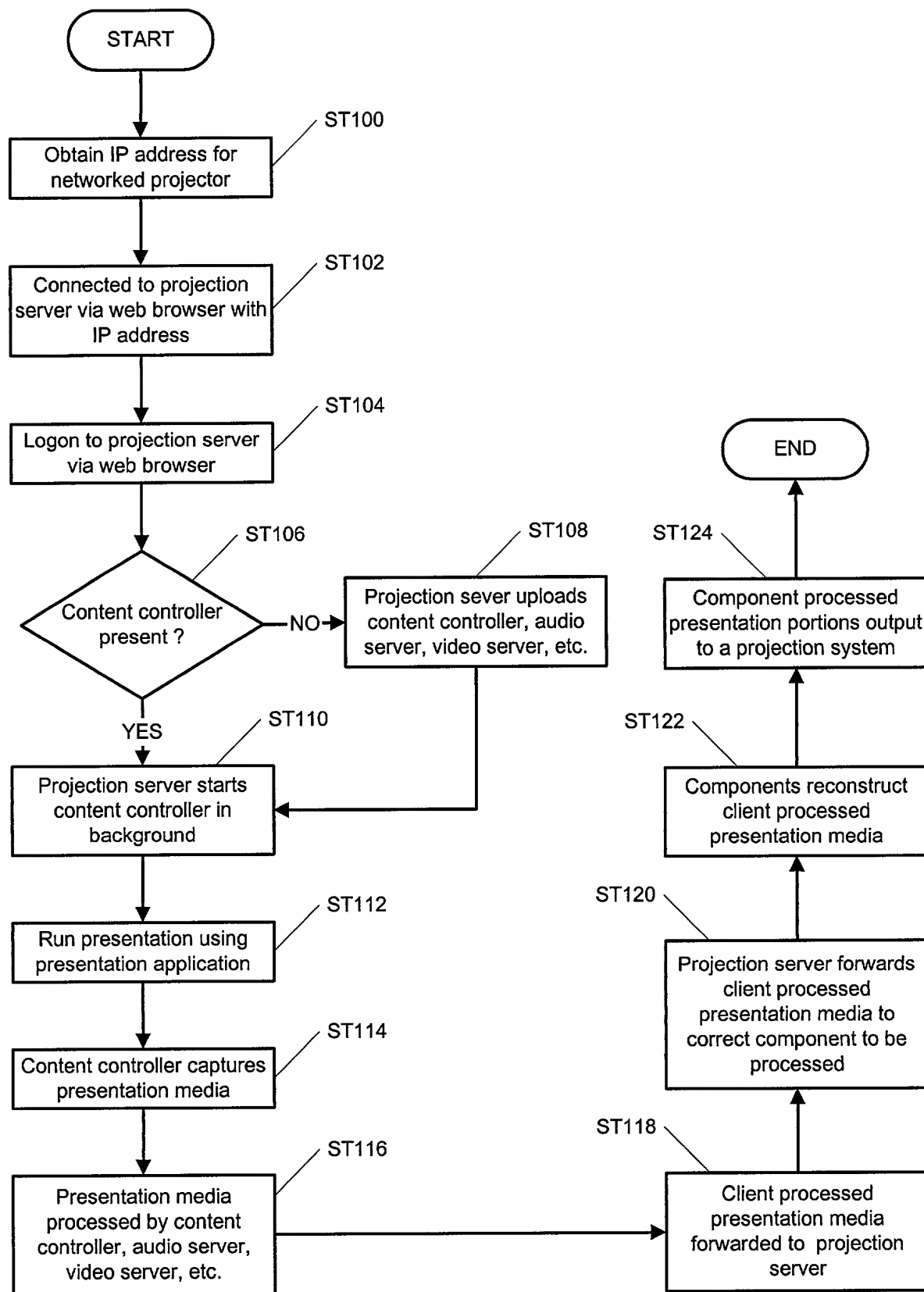


FIGURE 7